

11-07-06

CJC



PATENT  
Docket No. 51889/2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of )  
Douglas R. Hackler, Sr., et al. )  
Serial No.: 10/613,169 ) Art Unit  
Filed: July 3, 2003 ) 2814  
For: MULTI-CONFIGURABLE INDEPENDENTLY )  
MULTI-GATED MOSFET )  
Examiner: Phat X. Cao )  
Patent No.: 7,015,547 )  
Issue Date: March 21, 2006 )

REQUEST FOR A CERTIFICATE OF CORRECTION  
UNDER 37 C.F.R. § 1.323

Attn: Certificate of Correction Branch  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**Certificate**  
NOV 09 2006  
**of Correction**

Sir:

In accordance with the provisions of 37 C.F.R. § 1.323 which implement 35 U.S.C. §  
255, the Commissioner is respectfully requested to issue a Certificate of Correction to correct

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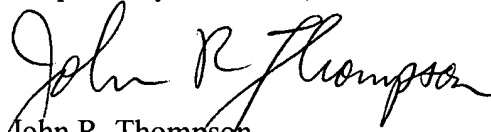
100.00 OP

NOV - 9 2006

mistakes in the above-identified patent. The enclosed Patent Office Form 1050 reflects the desired corrections.

Dated this 6 day of November, 2006.

Respectfully submitted,



John R. Thompson  
Attorney for Patentee  
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JRT:jbb  
Docket: 51889/2

NOV - 9 2006

<b>CERTIFICATE OF MAILING BY "EXPRESS MAIL" (37 CFR 1.10)</b> Applicant(s): <b>Douglas R. Hackler, Sr., et al.</b>			Docket No. <b>51889/2</b>	
Application No. <b>10/613,169</b>	Filing Date <b>July 3, 2003</b>	Examiner <b>Phat X. Cao</b>	Customer No. <b>32642</b>	Group Art Unit <b>2814</b>

Invention:

**MULTI-CONFIGURABLE INDEPENDENTLY MULTI-GATED MOSFET**

I hereby certify that the following correspondence:

See below

*(Identify type of correspondence)*

is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

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Certificate of Correction (2 pgs)

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UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,015,547 B2  
DATED : March 21, 2006  
INVENTOR(S): Douglas R. Hackler, Sr.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

**Column 3, line 33 "... FIG. 1 ..." change to --FIG. 1,--**

**Column 4, lines 23-24 "... coupling to the main bodies ..." change to --coupling the main bodies--**

**Column 5, line 2 "... material of the first ..." change to --material as the first--**

**Column 5, line 11 "... as a STI oxide, ..." change to --as an STI oxide,--**

**Column 7, line 41 "... A SOI layer ..." change to --An SOI layer--**

**Column 7, line 45 "... form a N+ ..." change to --form an N+--**

**Column 8, line 44 "... 38,40." change to --38, 40.--**

**Column 8, line 53 "... spacer 48, 50." change to --spacers 48,50.--**

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PATENT NO. 7,023,503 B2

No. of additional copies

➡ 1 of 2

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

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**Column 11, line 28 "... the drain and and proximate ..." change to -the drain and proximate—**

**Column 12, line 16 "... gate insulator and top ..." change to -gate insulator, and --**

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